J 5 H L

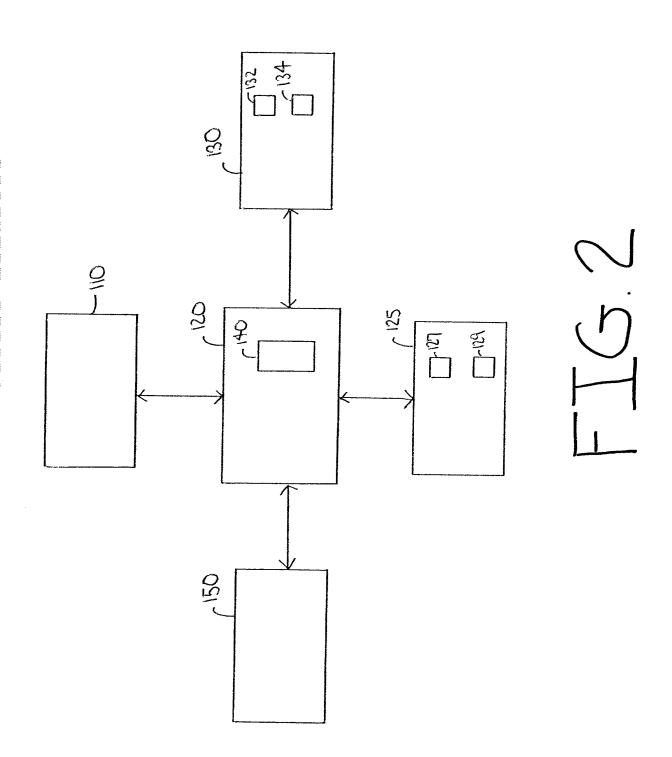
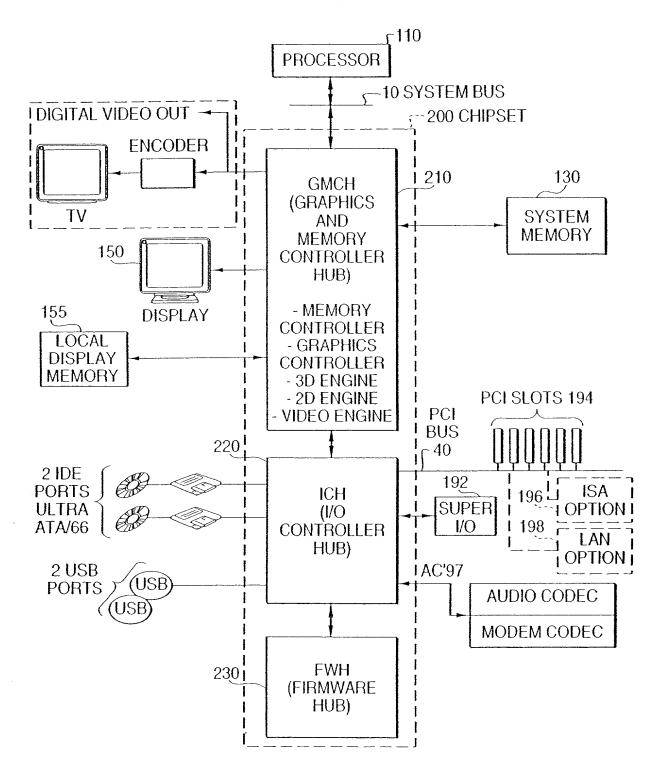
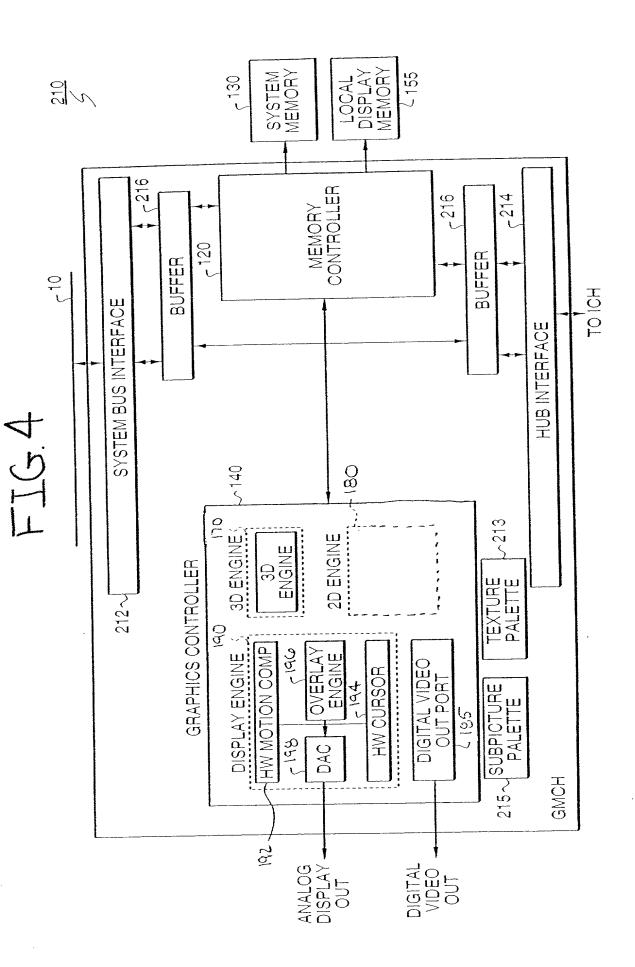
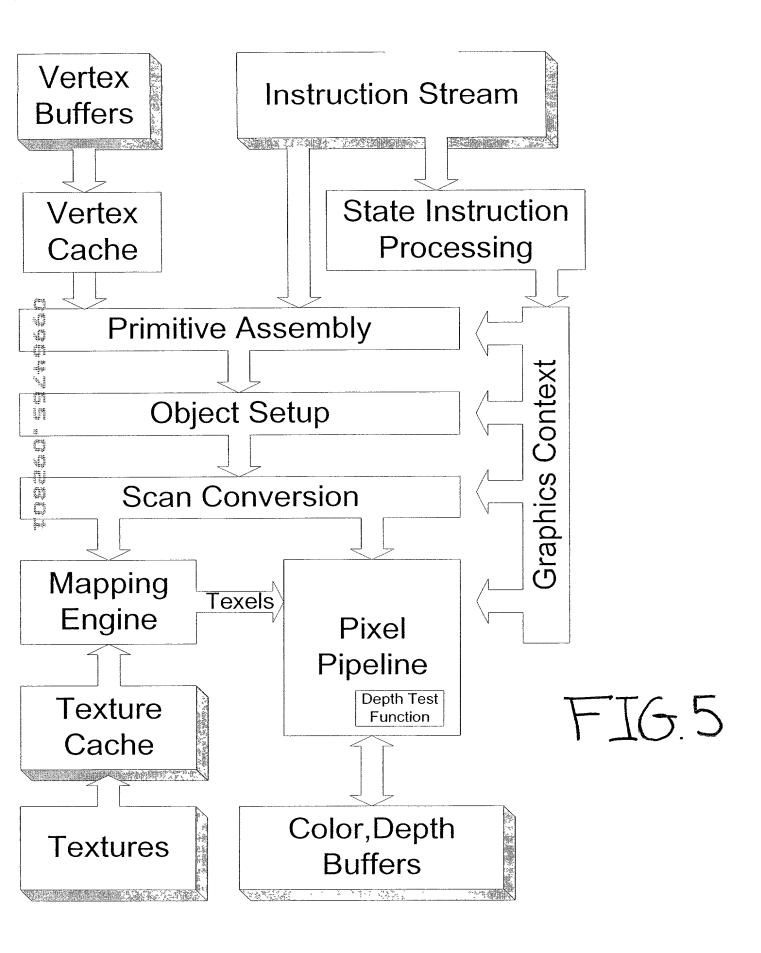


FIG.3







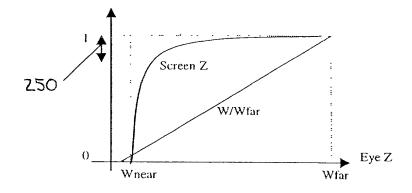


FIG.6

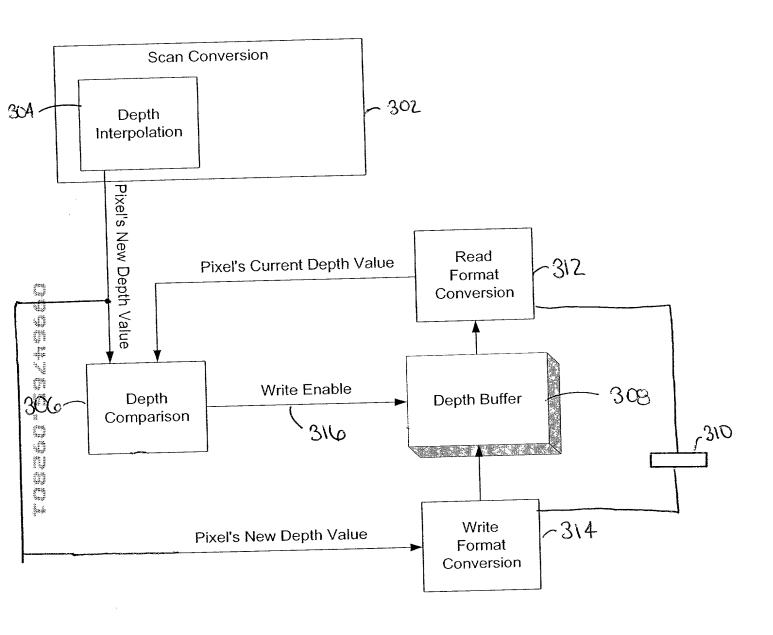


FIG. 1

15		16-n	15-n	0
	Biased Exponent			Fraction

Bit	Description
15:16-n	Biased Exponent: Format: n-bit unsigned biased exponent, where $n = WExponentSelect$. The exponent is biased by 2^n .
15-n:0	Fraction: Format: (16-n)-bit fractional portion of the floating point significand.

FIG.8B

	15	Normalized W	
		n tulian	
Bit		Description	
15:0	Normalized W (W/Wfar): Format: U0.16		İ
	Format: U0.16 Range = [0,1)		

FIG.8C

Biased Exponent	Significand		Represented Value	
(n bits)	Integer	Fraction	(W/WFar)	
$exp = 0 \ 2^{n}-1$	1	frac	1.frac * 2^(exp-2^n)	J

FIG.9A

31	24	23	24-n	23-n		0
	Stencil	Biased E	xponent		Fraction	

Bit	Description
31:24	Stencil:
	Format: U8
	Range = [0,255]
23:24-n	Biased Exponent:
	Format: n-bit unsigned biased exponent, where $n = WExponentSelect$. The exponent is biased by 2^n .
23-n:0	Fraction:
	Format: (16-n)-bit fractional portion of the floating point significand.

FIG.9B

	Stencil	Normalized W
Bit		Description
31:24	Stencil: Format: U8	
23:0	Range = [0,255] Normalized W (W/Wfar): Format: U0.24	
	Range = [0,1)	

31